



DOEACC CENTRE CALICUT
COURSE PROSPECTUS

CDS/CA/7.5.1/F 40/R0

Name of the Group	:	EMBEDDED SYSTEM
Name of the Course	:	PG Diploma in Embedded System Design
Course Code	:	ED 500
Starting Date	:	30 th August 2010
Duration	:	24 weeks

Preamble:

In today's increasing global market place, successful companies are finding that investments in hardware and software are no longer enough to maintain a competitive edge. Human elements with specialized engineering and design skills have become the essential part of the equation.

Embedded systems monitor and control everything from spacecraft to robots, microwave ovens, car engines, VCRs, television sets and much more. They control virtually everything that is electronic in our lives.

Embedded systems are normally built around Microcontrollers, Digital Signal Processors (DSPs) and FPGAs or SOCs. Embedded System Design course focuses on the architecture and programming of embedded devices such as Microcontrollers, DSPs and FPGAs and also on Embedded and Real-Time Operating Systems.

Objective of the Course:

To mould fresh electronics engineers and to retrain working engineers into High Caliber Embedded System Designers by enhancing their knowledge and skills in various hardware and software design aspects of Embedded Systems. This course offers a range of topics of immediate relevance to industry and makes the students exactly suitable for industries engaged in Embedded System development. This course is also an excellent preparation for those wishing to engage in application research in this rapidly developing area.

Outcome of the Course:

- *Participants get exposure with different families and architectures of Embedded System tools such as Microcontrollers, DSPs, FPGAs etc.*
- *Participants shall get the expertise required to design any embedded system (H/w or S/w or both) based on any of the above devices.*
- *Participants become highly proficient in Embedded Software particularly in real-time programming with industry standard RTOS such as VxWorks and RTLinux.*

Course Structure:

The ED500 course contains eight modules. After completing the first seven modules, the students have to do a six weeks project using any of the topics studied to get the PG Diploma certificate.

ED500	MODULE NAME	Weeks
ED 501	Embedded C and 8-bit Microcontrollers	4
ED 502	System Design using ARM Microcontrollers	3
ED 503	System Design using Digital Signal Processors	3
ED 504	Embedded Linux	2
ED 505	Embedded RTOS (RTLinux & VxWorks)	2
ED 506	Wireless Communication Systems and System Design using FPGAs	2
ED 507	Embedded Product Design	2
ED 508	Project Work	6
		24

Other Contents:

- a. **Course Fee:** Rs 50,000/- (service tax extra - presently @10.3%)
SC/ST candidates whose total annual family income is less than one lakh can avail concession of 25% of the course fee.
- b. **Eligibility :**
 1. M.E./M.Tech or B.E./B.Tech in Electronics/Electronics & Communication/ Electrical/Instrumentation/Biomedical/Computer Science/Information Technology or MSc in Electronics/ Instrumentation/ Computer Science/Information Technology.
 2. The Post Graduate Diploma certificate shall be issued to only those who complete the course successfully and produce original or provisional degree certificate and complete mark list.
 3. Candidates who have appeared in the qualifying examination and awaiting results may also apply. Such candidates shall be awarded the PG Diploma certificate only after successful completion of the course as well as on production of the degree or provisional certificate and complete mark list. If a candidate is appearing for the examinations (back papers) after the completion of the PG Diploma course (ED500) at our institute, he/she shall be eligible only for **Advanced Diploma** against PG Diploma on production of the qualifying degree result.
 4. On the date of counseling/admission, he/she must produce the original mark lists up to the last semester/year of examination.

- c. **Number of Seats : 40**
SC/ST candidates and Persons with disabilities are eligible for seat reservation as per existing rules.
- d. **How to Apply :**
Students are advised to apply in the prescribed Application Form available with the course brochure/prospectus or downloaded from our website. Filled in application forms along with a Demand Draft towards advance fee of Rs.1000/- *drawn in favour of* Director, DOEACC Centre, Calicut, *Payable at* State Bank of India, NITC Branch, Chathamangalam should be sent to the '**Training Officer, DOEACC Centre Calicut, P. B. No. 5, NIT Campus Post, CALICUT – 673 601, Kerala**'. **The Name of the Course applied for should be super scribed on the top of the cover in which the application form is sent.** Hostel accommodation (if required) can be reserved on payment of Rs.500/- along with the advance fee.
- e. **Selection of candidates :**
Selection of candidates will be based on their marks in the qualifying examination and the admission test conducted by DOEACC CENTRE subject to eligibility and availability of seats.
The admission to the course shall be based on the following criteria:
1. Should have passed the eligibility criteria as mentioned above.
 2. All students who have applied for the course will have to take an **online test conducted on 11th, 13th & 17th August 2010**. Students can attend the exam on any one of these dates according to their convenience.
 3. The admission test shall be of one hour duration with 50 questions. The test will be with Aptitude (20%), Logical reasoning (20%), C Programming (10%) and Basic Electronics (50%) including topics digital, analog, microprocessor, computer organization, signals & systems.
 4. The list of Provisionally Selected Students will be published in our website www.doeaccalicut.ac.in on 19th August 2010.
 5. **All candidates who are provisionally selected have to pay the full course fee (Rs 50,000/- + Service tax currently @10.3%) or first installment of the course fee (Rs 25,000/- + Service tax currently @10.3%) between 20th - 27th August 2010 by DD or direct payment into our account from any bank where core banking facility is available. The different Modes of Payments are detailed in the link <http://www.doeaccalicut.ac.in/course/calendarCEDTI.asp>. The proof of remittance of the fees by selected candidates shall be sent to trng@doeaccalicut.ac.in. The original receipt/counterfoil/chalan/DD has to be submitted at the centre as a proof of payment at the time of counseling.**
 6. The details required for direct payment are as given below.

Current Account No:	: 010401158037
Bank Name	: SBI, CNIT Chathamangalam
Bank Code	: 2207
IFS Code	: SBIN0002207

The depositor should obtain the **UTR Number** from the branch while depositing cash directly into our account. Depositor should also obtain the acknowledgement duly filled up and signed by the staff of the bank through which the amount was deposited. UTR number should be mentioned in all the correspondences to us pertaining to amount.

The following details are to be given by the depositor.

1. Name of the Depositor
2. Name of the Student
3. Date of Payment
4. Amount Deposited
5. Name of Bank/branch through which amount deposited
6. Purpose – Course ID – Advance Fee/Hostel Rent/Installment Fee etc.
7. Proof of Deposit (photocopy of receipt/counterfoil/chalan via email and original of the same at the time of counseling/admission)
8. UTR Number

The centre will not be responsible for any mistakes done by either the bank concerned or by the depositor while remitting the amount into our account.

7. **Counseling/Interview/Admission is on 30th August 2010.** Admission is subject to satisfying the requirements and availability of seats. Attending the counseling does not guarantee admission.

f. **Online Test:**

Admission test shall be conducted online on 11th, 13th & 17th August 2010.

The admission test shall be of one hour duration with 50 questions. The test shall be with Aptitude (20%), Logical reasoning (20%), C Programming (10%) and Basic Electronics (50%) includes topics on Digital, Analog, Microprocessor, Computer Organization, Signals and Systems. Students can appear for the on-line examination on any of the dates mentioned above according to their convenience. All the eligible students shall receive the user name and password by e-mail at least two days before the exam. Those students who are not getting the user name and password at least two days before the examination may contact the centre.

g. **Counseling/Admission :**

All candidates **provisionally selected** and **paid the fees** will have to be present personally for **counseling and admission on 30th August 2010** with all the necessary documents (originals and attested copies). Those who don't bring the necessary documents (originals and attested copies) on **30th August 2010** are not eligible for admission and counseling.

The original certificates and mark lists starting from SSLC or equivalent up to the present qualifying degree have to be produced for verification whereas the attested copies should be submitted here. The classes shall commence on **30th August 2010**. Those candidates who have paid the fees before counseling and are not selected for the course are eligible for refund of the fees on production of the **original** receipt/counterfoil/chalan as proof of fee payment.

h. **Admission Procedure :**

All Candidates who have been provisionally selected for the course are required to report at the Centre on the prescribed day by 9:30 hrs for **counseling and admission** along with the following documents

1. Attested Copies of Proof of Age (10th), Qualifications(MTech/BTech/MSc etc)
2. Original Certificates of the above (Provisional/Degree & Mark list)
3. Two copies of passport size photograph and one stamp size photograph
4. SC/ST Certificate in English/Hindi (if applicable)
5. Income Certificate English/Hindi (if applicable)

The candidates on reaching the Centre are required to meet the Front Office Counselor (FOC). The FOC then directs the candidates to the Course Coordinator. The candidate gets the enrollment form and certificates verified by the Course Coordinator. The selected students will then have to meet the FOC along with the enrollment form, who shall direct the student to the Accounts. The candidates are **required to produce the original receipt/counterfoil/chalan as proof of fee payment** at the accounts and get the fee receipt issued by the concerned authority. A student is thus admitted and can attend the classes.

i. **Discontinuing the Course :**

No fees under any circumstances shall be refunded in the event of a student discontinuing the course.

j. **Course Timings :**

This program is a practical oriented one and hence there shall be more lab than theory classes. The classes and labs are from 9.30 am to 12.45 pm and 1.30 pm to 5.30 pm Monday to Friday. During project work, the timings are from 9.15 am to 5.30 pm. The theory to lab proportion is 30:70.

k. **Location & How to reach:**

DOEACC CENTRE Calicut is located very near to NIT (REC) Campus and is about 22Kms from the Calicut (Kozhikode) city. A number of buses [Buses to NIT via Kunnamangalam] are available from "Palayam Bus Stand or KSRTC Bus Stand". Our stop is called "Panthrand" & is one stop before NIT. The bus fare is currently Rs.10.50 from Calicut City to DOEACC Centre. Calicut (Kozhikode) is well connected by Rail, Road and Air from different parts of the country.

l. **Course enquiries :**

Students can enquire about the various courses either on telephone or by personal contact between 9.15 A.M. to 5.15 P.M. (Lunch time 1.00 pm to 1.30 pm).

Telephone Numbers:

Office	0495 - 2287266 / 2287268
Director's Office	0495 - 2287123
Training Officer	0495 - 2287266
ED500 Course Coordinator	0495 - 2287178
Office Fax	0495 - 2287168

E-mail:

trng@doeaccalicut.ac.in

Website:

www.doeaccalicut.ac.in

m. **Important Dates (if applicable) :**

Last date of receiving completed application forms	: 16 th August 2010
Admission Test	: 11 th , 13 th & 17 th August 2010
Selection list in website	: 19 th August 2010
Payment of first installment fees	: 20 th - 27 th August 2010
Counseling/Admission	: 30 th August 2010
Commencement of classes	: 30 th August 2010
Payment of second installment fees	: 29 th November 2010

n. **Placement :**

We have a placement cell, which provides placement assistance to students who qualify our courses. The previous ED500 batches have good placement records and are placed in reputed firms such as WIPRO-Bangalore, WIPRO-Cochin, PROCSYS-Bangalore, Mistral-Bangalore, Premier Evolvics-Coimbatore, Tata Elixsi-Trivandrum, Robert Bosch-Bangalore, BPL Telecom-Bangalore, BPL Telecom-Palghat, Mind Tree-Bangalore, GDATech-Chennai, GDATech-Cochin, Vorroc-Pune, Virtual wires-Delhi, Wings-Faridabad etc.

The course has helped our students to get good opportunities and most of them are well placed. The course improves the knowledge and skill of the students as it deals with the latest technologies and tools used in industries. This helps the student in getting a placement by

- a. Campus placement
- b. Placement by companies for whom we send the students bio data and they conduct interviews at their site.
- c. Students themselves attend interview at different companies and the course helps in the interview.

o. **Hostel facilities :**

Hostel accommodation is available for boys and girls on daily or monthly chargeable basis. The hostel fee varies from 450/- to 900/- per month depending on the location of accommodation and facilities. However, students are required to pay the hostel fees for the duration of the course for which they are seeking admission at the time of joining the course.

p. **Canteen facilities :**

The Centre has a canteen functioning at the main campus and food at reasonable rates is available for breakfast, lunch, and dinner.

q. **Lab Facilities :**

We have state-of-the-art lab facility in Embedded Systems which include,

- 8-bit & 16-bit Microcontroller Dev. Systems - Intel 8051, 80C196, MPLAB for PIC 16 & 18 series, Cygnal etc
- 32-bit ARM9 microcontroller Development Systems – ATMEL & CIRRUS LOGIC
- 'C' compiler for 8051 (KEIL 'C' Development IDE), PIC (CCS), 80C196 (Tasking), ARM Developer Suite (ADS v1.)
- TI DSP Development Systems – 'C6000, 'C5400, 'C5500, 'C243, 'C2812, 'C32, 'C50 etc.
- AD DSP Development Systems - 2191, 21065, 21061 Sharc DSPs
- Code Composer Studio, Visual DSP
- VxWorks, RTLinux & QNX RTOS, Embedded Linux
- Xilinx ISE FPGA Design Tools, Leonardo Spectrum, Model Sim Simulator
- Matlab, Simulink, TI 'C6000 target for Matlab, LabView, Wireless LAN
- Universal DATA I/O programmer, PC Based EDA tools (ORCAD)
- Digital Storage & Mixed Signal Oscilloscopes (500, 350, 300, 100 MHz)
- EMI Test Setup, Logic Analyzer, SMD Rework station

r. **Course Contents :**

ED 501 Embedded C and 8-bit Microcontrollers

Module Duration: 20 days

Objective

This module is framed to set the required background in ‘C’ Language for the rest of the modules on embedded software. It aims at familiarising the students in programming in ‘C’. This module covers the advanced topics in ‘C’ such as Memory management, Pointers, Data structures which are of high relevance in embedded software are considered in depth. This module makes use KEIL C Compiler along with 8051 microcontrollers.

This module also covers the architecture of the popular 8-bit Microcontrollers such as 8051, and PIC for lower end applications. Microcontrollers with built-in peripheral functions (such as PWMs for Motor Control or UPS, interface for Quadrature Encoders, Frequency measurement, Serial ports, 10-bit 8-channel ADCs, Digital I/Os, EPROM, RAM etc.), and lower power consumption are very popular and have drastically altered the economics of Digital System Design. The low cost, small size and programmability make them suitable for an enormous number of applications in products and systems like fuzzy logic controllers, industrial automation, home appliances etc. 8-bit and 16-bit Microcontrollers are widely used in industrial applications.

This module covers hardware and software design aspects in detail. The embedded system design aspects using 8-bit Microcontrollers are also covered in detail.

Course Description

a) Embedded C

All relevant aspects of ‘C’ programming under embedded environment are dealt with. It starts with the basics of ‘C’ language and covers the advanced topics in detail.

Course contents

‘C’ programming

Storage Classes, Data Types, Controlling program flow, arrays, functions

Memory Management

Pointers, Arrays and pointers

Pointer to functions and advanced topics on pointers

Structures and unions

Data structures

Linked List, Stacks, Queues

Conditional Compilation, Preprocessor directives, File operations, Variable arguments in Functions, Command line arguments, Bitwise operations, Typecasting

b) 8051 Microcontrollers

- Architecture of 8051 Family of Microcontrollers
- Assembly Language Programming of 8051
- Peripherals of 8051 Family of Microcontrollers
- System Design using 8051 Family of Microcontrollers
- Programming includes Keyboard Interfacing, LCD Interfacing, ADC and DAC, On chip Timers and Serial port
- Mini Project using 8051 Family of Microcontrollers

Device Platform: SBC-51 Development Board, Keil ‘C’ Compiler & Windows based Wise-51 Software.

Programming Language: ‘C’ and Assembly Language

c) PIC Microcontrollers

- Architecture of PIC Family of Microcontrollers (18F series)
- Programming of PIC Family of Microcontrollers
- Peripherals of PIC Family of Microcontrollers

Device Platform: MPLAB Simulator & 18F452 Dev Board

Learning Outcomes

After successful completion of the module students should be able to:

- Develop advanced programs in Embedded ‘C’
- Understand the architecture of the various types of 8-bit Microcontrollers
- Understand the programming of the various types of 8-bit Microcontrollers
- Understand the hardware interfacing of the peripherals to Microcontrollers
- Select a proper Microcontroller for a particular application
- Design new embedded systems using 8-bit Microcontrollers

Reading List

1. Let us C by Yashwant Kanetkar
2. Embedded C, Pont, Michael J
3. C Programming by Worthington, Steve
4. C Programming language, Kernighan, Brian W, Ritchie, Dennis M
5. Art of C Programming, JONES, ROBIN, STEWART, IAN
6. C Programming for Embedded systems, Zurell, Kirk
7. C and the 8051 Programming for Multitasking – Schultz, Thomas W
8. 8051 Microcontroller and Embedded Systems – Mazidi, Muhammad Ali, Mazidi, Janice Gillispie
9. Embedded Microcontrollers – Intel Hand Book
10. Programming and Customizing the 8051 microcontroller – Predko, Myke

11. 8051 Microcontroller: Hardware, Software and Interfacing – Stewart, James W,
Miao, Kai X
12. C and the 8051: Hardware, Modular Programming and Multitasking Vol i –
Schultz, Thomas W
13. 8051 microcontroller: Architecture, Programming & Applications – Ayala,
Kenneth J
14. Programming and Customizing PIC Microcontroller – Predko, Myke
15. Design with PIC Microcontrollers – John B Peatman
16. PIC Microcontroller Project Book - Lovine, John

ED 502: System Design using ARM Microcontrollers**Module Duration:** 15 days**Objective**

The 32-bit Micro-controllers with 'C' language support and multifunction peripherals are being used in most of the medium and high end Embedded applications such as Mobile computing, Motion control, Wireless communication and even in Signal processing etc. The processors provide high end computing power as well as an extensive array of peripherals such as USB, LAN support, UART, Modem support, LCD/Graphics interface, General purpose I/O, I²C, CAN etc.

The 32-bit ARM processors are used in high end computation requirements such as Multimedia, Digital Camera and other high end applications. ARM processors are very popular in Industries. This module focuses on the architecture of the 32-bit ARM9 Microcontroller. The assembly language programming as well as 'C' language programming of the controller is also dealt in detail. The ARM Developer Suite with Multi ICE and ARM9 kit from Cirrus Logic is used.

Course Description

- Introduction to ARM Family of Microcontrollers
- Architecture of ARM9 Microcontrollers
- Architecture of Cirrus Logic EDB9302/Atmel AT91RM9200 Family of Microcontrollers
- 'C' & Assembly Language Programming of ARM Microcontrollers
- ARM and Thumb Inter-working
- Peripherals of EDB9302/AT91RM9200 ARM9 Microcontrollers
- Mini Project

Device Platform: Arm Developer Suite 1.2, Multi ICE and ARM9TDMI boards (EDB9302 & EDB9315 from Cirrus Logic, AT91RM9200 from ATMEL)

Programming Language: Assembly Language & 'C'

Learning Outcomes

After successful completion of the module students should be able to:

- Understand the H/w and S/w issues related to the design of a Microcontroller based system catering to the needs of medium and higher end applications.
- Understand the architecture and programming of the 32-bit ARM Processors

Reading List

1. ARM System Developer's Guide - Designing and Optimizing System Software by: Andrew N Sloss, Dominic Symes, Chris Wright; 2004, Elsevier
2. ARM Architecture Reference Manual by: David Seal, 2001 Addison Wesley, ENGLAND
3. ARM System - On - Chip Architecture, Furber, Steve

ED 503: SYSTEM DESIGN USING DIGITAL SIGNAL PROCESSORS

Module Duration: 15 days

Objective

Digital signal processing techniques are so powerful that sometimes it is extremely difficult for analogue signal processing to achieve the same or closer performance. This module introduces the digital signal processing fundamentals like convolution, DFT, FFT, Spectrum analysis etc. 'C' language will be used as the language for code development. The fundamentals of 'C' programming will also be covered in this module.

MATLAB is a powerful tool for design and analysis of digital signal processing algorithms. The capabilities of MATLAB tool will also be explored during the course.

The participants of this module will learn the architecture and programming of TMS320C5510 DSPs and Analog Devices SHARC DSPs. The hardware and complete implementation of selectable algorithms are dealt in detail with C5510 DSP. Programming with peripherals of this DSP is also covered in the module. Code Composer Studio IDE from Texas Instruments and Visual Studio from Analog Devices shall be used as the programming and debugging tools. A mini-project is also part of this course.

Course Description

- Introduction to DSP Fundamentals
- FFT, Filter Design
- Introduction to MATLAB
- DSP Programming using MATLAB
- Introduction to DSP processors
- Introduction to Code Composer Studio (CCS)
- Architecture and Programming of 'C5510 DSP
- Peripherals of 'C5510 DSP
- Architecture of SHARC DSP
- Introduction to Visual DSP++
- Programming of SHARC DSP

Learning Outcomes

After successful completion of this module, students should be able to:

- Understand the Fundamental algorithms in Digital Signal Processing.
- Understand the MATLAB programming Language
- Understand the use of MATLAB for DSP applications

- Implement the Fundamental DSP algorithms using the ‘C’ programming language.
- Understand the architecture and programming of the ‘C5510 DSP.
- Understand the Fundamental algorithms in Digital Signal Processing and their implementation on the above DSPs.
- Will get familiarized with the Code Composer studio and Visual DSP Development Environment for DSP programming and debugging.

Reading List

- Digital Signal Processing Design- Andrew Bateman, Warren Yates
- Introduction to Digital Signal Processing - John G Proakis, Dimitris G Manolakis
- Introduction to Digital Signal Processing - Johnny R Johnson
- Digital Signal Processing: A System Design Approach - David J Defatta
- Digital Signal Processing Laboratory Using MATLAB – Sanjit K. Mitra
- Digital Signal Processing - P Ramesh Babu
- Real - Time Digital Signal Processing: Implementations, Applications, and Experiments with the TMS320C55X, Kou, Sen M, Lee, Bob H

ED504: Embedded Linux

Module Duration: 10 days

Objective

The objective of the course is to provide understanding of the techniques essential to the design and implementation of embedded systems with embedded operating systems.

Course Description

- **Introduction**
 - Linux as Embedded Operating System
 - Comparison of Embedded OS
 - Embedded OS Tools and development
 - Discussion on Embedded OS Applications and products

- **System architecture of a Basic OS**
 - Internals of Linux OS
 - System Calls, Linux Compiler options, Make
 - Process, Multithreading and Synchronization
 - Serial port and Network programming with embedded Linux
 - Interrupt programming and Kernel module programming
 - Device drivers

- **Inter Process Communication**
 - Pipe and FIFOs, Shared memory, Sockets

- **Getting Linux on a device**
 - Linux boot sequence, Building Kernel, Building Boot image

- **Linux porting on ARM**
 - Building root file system, Kernel Compilation for ARM, Porting of Embedded Linux to ATMEL ARM9 kit.

- **Practical Sessions**
 - Embedded Linux Applications

Learning Outcomes

After successful completion of the module, the students shall be able to:

- Understand the Embedded operating systems that is needed to run embedded systems
- Understand Embedded Linux and its internals
- Build embedded systems using Embedded Linux operating systems

Reading List

1. GNU/LINUX Application Programming, Jones, M Tims
2. Embedded Linux: Hardware, Software, and Interfacing, Hollabaugh, Craig,
3. Building Embedded Linux Systems: Yaghmour, Karim
4. Embedded Software Primer: Simon, David E.
5. Linux Kernel Internals: Beck, Michael At Al
6. UNIX Network Programming : Steven, Richard
7. Linux: The Complete Reference: Petersen, Richard
8. Linux Device Drivers: Rubini, Alessandro, Corbet, Jonathan
9. Linux Kernel Programming: Algorithms and Structures of version 2.4: Beck, Michael At Al
10. Linux Kernel Development: Love, Robert

ED 505: Embedded RTOS (RTLinux & VxWorks)

Module Duration: 10 days

Objective

The objectives of the course is to provide the students with an understanding of the aspects of the Real-time systems and Real-time Operating Systems and to provide an understanding of the techniques essential to the design and implementation of real-time embedded systems.

Course Description

- **Introduction**
 - Embedded Software – Real-time Vs Non Real-time
 - Introduction to Real-time systems and Embedded Real-time Systems
 - Discussion of popular RTOS like RTLinux and VxWorks
 - Comparison of Embedded RTOSs (RTLinux and VxWorks)
 - Design Goals for Real-time software
 - Discussion on Embedded Real-time applications
 - Considerations for real-time programming

- **System architecture of RTLinux**
 - Introduction RTLinux
 - Thread Creation and Management
 - Thread Synchronization Mechanisms
 - IPC – RTFIFO, Shared Memory
 - Interrupt Handling

- **System architecture of VxWorks**
 - Introduction to VxWorks
 - Task Creation and management
 - Inter Task Communication Mechanisms
 - Semaphores, Message Queues, Pipes
 - Interrupts, Tornado tools

- **Practical Sessions**
 - Application Development under RTLinux and VxWorks

Learning Outcomes

After successful completion of the module, the students shall be able to:

- Understand the Embedded Real Time software that is needed to run embedded systems
- Understand the RTLinux RTOS and their commands
- Understand the VxWorks RTOS and realtime application programming with it.
- Build real-time embedded systems using RTLinux and VxWorks real-time operating systems

Reading List

1. Embedded Systems Architecture Programming and Design: Raj Kamal, Tata McGraw Hill
2. Embedded/Real Time Systems Concepts, Design and Programming Black Book, Prasad, KVK
3. Software Design for Real-Time Systems: Cooling, J E Proceedings of 17th IEEE Real-Time Systems Symposium December 4-6, 1996 Washington, DC: IEEE Computer Society
4. Real-time Systems – Jane Liu, PH 2000
5. Real-Time Systems Design and Analysis : An Engineer's Handbook: Laplante, Phillip A
6. Structured Development for Real - Time Systems V1 : Introduction and Tools: Ward, Paul T & Mellor, Stephen J
7. Structured Development for Real - Time Systems V2 : Essential Modeling Techniques: Ward, Paul T & Mellor, Stephen J
8. Structured Development for Real - Time Systems V3 : Implementation Modeling Techniques: Ward, Paul T & Mellor, Stephen J
9. Monitoring and Debugging of Distributed Real-Time Systems: TSAI, Jeffrey J P & Yang, J H
10. Embedded Software Primer: Simon, David E.

ED 506: Wireless Communication Systems and System Design Using FPGAs

Module Duration

- 10 days

Objective

Being the backbone for all the IT based developments; Data communication has seen tremendous growth in the past decade. There are new techniques and protocols emerging from time-to-time to cater the requirements of this rapidly growing area. It has a very important role in embedded systems also, because many of the present-day embedded devices are connected to one or the other type of Network at the device level. Some of them use wireless connectivity also. In this context, it is important to have a thorough understanding of the Data Communication, Wireless Technologies, Systems and Protocols.

This module includes fundamentals of data communications, discusses about LAN and WAN technologies, Switching and Routing. Then the wireless systems of GSM and CDMA, short-range wireless systems such as WLAN and BlueTooth and wireless-web connectivity are discussed.

The course is structured to include the learning of VHDL syntax and the architecture of most prominent vendor in the FPGA market, Xilinx FPGAs. Hands own experiments and a mini-project are included in the module

Course Description

Wireless Communication Systems

- Data Communication Fundamentals
- TCP/IP suite of protocols
- LAN and WAN Technologies
- Switching and Routing
- CSMA/CD, Ethernet, Token Bus and Ring
- IP Services and Routing
- TCP and UDP
- Elements of wireless communication systems
- ISM Bands and Spread Spectrum Technologies
- Cellular Systems
- TDMA and CDMA based cellular systems
- GSM, GPRS
- BlueTooth and Personal Area Networks
- WLAN Technologies (IEEE 802.11 Series)

Practical Session

- WLAN, IP Services and Routing Simulation

Course Description

System Design Using FPGAs

- Introduction to VHDL
- VHDL Concepts, Types & Operators
- Sequential & Concurrent Statements
- VHDL Simulation
- XILINX FPGA Architecture
- Synthesis & Implementation on FPGAs
- Mini Project using FPGAs

Learning Outcomes

On completion, the participants will learn about:

- Use of Asynchronous and Synchronous modes of Data communication
- Using Ethernet for different high-level protocols
- Using wireless mode of Data Transmission
- Write the VHDL Program for any application
- Understand the Architecture of Xilinx FPGAs
- Design Embedded Systems based on Xilinx FPGAs using VHDL

Reading List:

1. Computer Networks; By: Tanenbaum, Andrew S; Pearson Education Pte. Ltd., Delhi, 4th Edition
2. Data And Computer Communications; By: Stallings, William; Pearson Education Pte. Ltd., Delhi, 6th Edition
3. Data Communications and Networking; B. A. Forouzan, McGraw Hill, 2nd edition, 2001
4. TCP/IP Illustrated, Volume 1: The protocols, Addison By W. R. Stevens; Wesley, 1994.
5. TCP/IP Illustrated, Volume 2: The Implementation, By G. R. Wright; Addison Wesley, 1995.
6. TCP/IP Illustrated, Volume 3: TCP for Transactions, HTTP, NNTP, and the Unix Domain Protocols, By W. R. Stevens; Addison Wesley, 1996.
7. Wireless Communications – Principles and Practice; by Theodore S Rappaport, Pearson Education Pte. Ltd., Delhi
8. Wireless Communication Technology; By: Blake, Roy; Delmar, New York.
9. Wireless Communications and Networking; By: Stallings, William; Pearson Education Pte. Ltd., Delhi
10. Bluetooth Revealed; By: Miller, Brent A, Bisdikian, Chatschik; Addison Wesley Longman Pte Ltd., Delhi
11. SNMP, SNMPV2, SNMPV3, AND RMON 1 AND 2 by Stallings, William; Addison Wesley Longman (Singapore) Pte Ltd., Delhi

12. High - Speed Networks And Internets: Performance And Quality Of Service, by Stallings, William; Pearson Education Pte. Ltd., Delhi
13. Mobile And Personal Communications Services And Systems; 1st Edition; By: Raj Pandya; PHI, New Delhi
14. Mobile Communications Engineering; Theory And Applications, By: Lee, William C Y; MGH, New York
15. Mobile Communications; By: Schiller, Jochen H; Addison Wesley Longman Pte Ltd., Delhi
16. Essential Guide To Wireless Communications Applications: From Cellular Systems To Wap And M - Commerce; By: Dorman, Andy; Addison Wesley Longman Pte Ltd., Delhi
17. Wap, Bluetooth, And 3G Programming: Cracking The Code; By: Dreamtech Software Team; IDG Books India (P) Ltd., New Delhi
18. Mobile And Personal Communications Services And Systems; 1st Edition; By: Raj Pandya; PHI, New Delhi
19. Essential Guide To Wireless Communications Applications: From Cellular Systems To WAP And M - Commerce; By: Dorman, Andy; Addison Wesley Longman Pte Ltd., Delhi
20. Bluetooth Demystified; By Nathan J. Muller, McGraw-Hill TELECOM Publishing company Ltd., New Delhi
21. VHDL Analysis and Modeling of Digital Systems – Navabi, Zainalabedin
22. Guide to VHDL Syntax – Bhasker, j
23. VHDL - Perry, Douglas
24. Digital Systems Design with VHDL and Synthesis – An Integrated Approach – Chang, K C
17. Designer's guide to VHDL - Ashenden, Peter J
18. Programmable Logic Devices Databook and Design Guide – National Semiconductor
19. Digital System Design using Programmable Logic Devices – Lala, Parag K
20. Programmable Logic Databook – Xilinx Inc.

ED 507: Embedded Product Design

Module Duration

- 10 days

Objective

The objective of this module is to help fresh graduates and practicing engineers to enhance their knowledge and skills of embedded product design covering various dimensions of product development, Quality principles and tools, Project Management etc and discussion with suitable case study.

Course Description

- Quality principles and tools
- Product Development Process
 - System level design using hardware and software
 - Hardware and software integration issues and testing
 - Hardware and software coverification
 - Component cost and costing in product design
 - Case studies of real life designs
- Industrial Design
- Project Management (PERT/CPM) MS Project
- Interconnection design & EDA tools
- Thermal Design
- Documentation
- Team work and communication
- Embedded Product design Syndicate
- EMI/EMC
- **Case study of Microcontroller based Design**
 - Project Design phase
 - Hardware design and construction
 - Software design and development
 - Integration and debugging of hardware and software
 - Final testing
 - ORCAD Schematic and PCB Layout
- Mini Project

Learning Outcomes

After successful completion of the module, the students shall be able to understand and practice quality principles and tools in product development process, right from identifying customer requirements and translating them into product specifications and realization of the product specifications through electronics, mechanical and industrial design, product engineering and meeting the cost and development time constraints through better project management.

Students shall Design and Develop a standalone Data Acquisition System using Microcontroller. Conceptual design, PCB Design, PCB Assembly, Testing, Integration etc. are covered in the mini project

Reading List

1. Product Design & Development - Karl T Ulrich & Steven D. Eppinger; Mc Graw Hill
2. Total quality management Besterfield, Dale H
3. Relevant Data sheets and application notes

ED 508: Project Work

Module Duration

- 6 Weeks

Course Description

The students can select hardware, software or system level projects. The project can be implemented using **Microcontroller or DSP or FPGA or RTOS** tools which students have studied and used during the course. A total product or project can be selected.